

## 5.105 cycle\_or\_accessibility

	DESCRIPTION	LINKS	GRAPH
<b>Origin</b>	Inspired by [244].		
<b>Constraint</b>	cycle_or_accessibility(MAXDIST, NCYCLE, NODES)		
<b>Arguments</b>	MAXDIST : int NCYCLE : dvar NODES : collection(index-int, succ-dvar, x-int, y-int)		
<b>Restrictions</b>	MAXDIST $\geq 0$ NCYCLE $\geq 1$ NCYCLE $\leq  \text{NODES} $ required(NODES, [index, succ, x, y]) NODES.index $\geq 1$ NODES.index $\leq  \text{NODES} $ distinct(NODES, index) NODES.succ $\geq 0$ NODES.succ $\leq  \text{NODES} $ NODES.x $\geq 0$ NODES.y $\geq 0$		
<b>Purpose</b>	Consider a digraph $G$ described by the NODES collection. Cover a subset of the vertices of $G$ by a set of vertex-disjoint circuits in such a way that the following property holds: for each uncovered vertex $v_1$ of $G$ there exists at least one covered vertex $v_2$ of $G$ such that the Manhattan distance between $v_1$ and $v_2$ is less than or equal to MAXDIST.		
<b>Example</b>	$3, 2, \left( \begin{array}{cccc} \text{index} - 1 & \text{succ} - 6 & x - 4 & y - 5, \\ \text{index} - 2 & \text{succ} - 0 & x - 9 & y - 1, \\ \text{index} - 3 & \text{succ} - 0 & x - 2 & y - 4, \\ \text{index} - 4 & \text{succ} - 1 & x - 2 & y - 6, \\ \text{index} - 5 & \text{succ} - 5 & x - 7 & y - 2, \\ \text{index} - 6 & \text{succ} - 4 & x - 4 & y - 7, \\ \text{index} - 7 & \text{succ} - 0 & x - 6 & y - 4 \end{array} \right)$		
	Figure 5.232 represents the solution associated with the example. The covered vertices are coloured in blue, while the links starting from the uncovered vertices are dashed. The cycle_or_accessibility constraint holds since: <ul style="list-style-type: none"> <li>• In the solution we have NCYCLE = 2 disjoint circuits.</li> <li>• All the 3 uncovered nodes are located at a distance that does not exceed MAXDIST = 3 from at least one covered node.</li> </ul>		
<b>Typical</b>	MAXDIST $> 0$ NCYCLE $<  \text{NODES} $ $ \text{NODES}  > 2$		

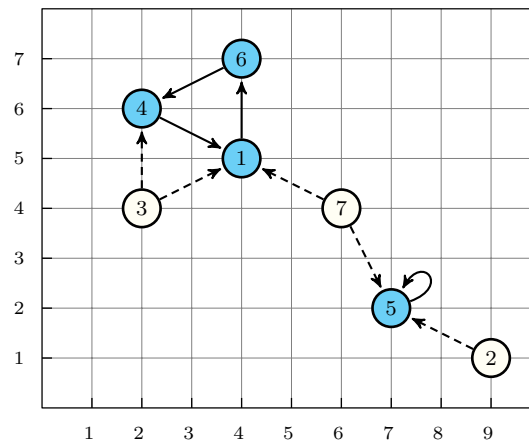


Figure 5.232: Final graph associated with the facilities location problem

#### Symmetries

- Items of NODES are [permutable](#).
- Attributes of NODES are [permutable](#) w.r.t. permutation (`index`) (`succ`) (`x`, `y`) (*permutation applied to all items*).
- One and the same constant can be [added](#) to the `x` attribute of all items of NODES.
- One and the same constant can be [added](#) to the `y` attribute of all items of NODES.

#### Arg. properties

[Functional dependency](#): NCYCLE determined by NODES.

#### Remark

This kind of facilities location problem is described in [244, pages 187–189] pages. In addition to our example they also mention the cost problem that is usually a trade-off between the vertices that are directly covered by circuits and the others.

#### See also

[common keyword](#): `cycle` (*graph constraint*).  
[used in graph description](#): `nvalues_except_0`.

#### Keywords

[constraint type](#): graph constraint.  
[final graph structure](#): strongly connected component.  
[geometry](#): geometrical constraint.  
[modelling](#): functional dependency.  
[problems](#): facilities location problem.

<b>Arc input(s)</b>	NODES
<b>Arc generator</b>	<code>CLIQUE</code> $\mapsto$ <code>collection(nodes1, nodes2)</code>
<b>Arc arity</b>	2
<b>Arc constraint(s)</b>	<code>nodes1.succ = nodes2.index</code>
<b>Graph property(ies)</b>	<ul style="list-style-type: none"> <li>• <code>NTREE</code> = 0</li> <li>• <code>NCC</code> = <code>NCYCLE</code></li> </ul>
<b>Arc input(s)</b>	NODES
<b>Arc generator</b>	<code>CLIQUE</code> $\mapsto$ <code>collection(nodes1, nodes2)</code>
<b>Arc arity</b>	2
<b>Arc constraint(s)</b>	$\bigvee \left( \begin{array}{l} \text{nodes1.succ} = \text{nodes2.index}, \\ \bigwedge \left( \begin{array}{l} \text{nodes1.succ} = 0, \\ \text{nodes2.succ} \neq 0, \\ \text{abs}(\text{nodes1.x} - \text{nodes2.x}) + \text{abs}(\text{nodes1.y} - \text{nodes2.y}) \leq \text{MAXDIST} \end{array} \right) \end{array} \right)$
<b>Graph property(ies)</b>	<code>NVERTEX</code> = <code> NODES </code>
<b>Sets</b>	<code>PRED</code> $\mapsto$ $\left[ \begin{array}{l} \text{variables} - \text{col} \left( \begin{array}{l} \text{VARIABLES} - \text{collection}(\text{var} - \text{dvar}), \\ [\text{item}(\text{var} - \text{NODES.succ})] \end{array} \right), \\ \text{destination} \end{array} \right]$
<b>Constraint(s) on sets</b>	<code>nvalues_except_0(variables, =, 1)</code>

**Graph model**

For each vertex  $v$  we have introduced the following attributes:

- `index`: the label associated with  $v$ ,
- `succ`: if  $v$  is not covered by a circuit then 0; If  $v$  is covered by a circuit then index of the successor of  $v$ .
- `x`: the x-coordinate of  $v$ ,
- `y`: the y-coordinate of  $v$ .

The first graph constraint forces all vertices, which have a non-zero successor, to form a set of `NCYCLE` vertex-disjoint circuits.

The final graph associated with the second graph constraint contains two types of arcs:

- The arcs belonging to one circuit (i.e., `nodes1.succ = nodes2.index`),
- The arcs between one vertex  $v_1$  that does not belong to any circuit (i.e., `nodes1.succ = 0`) and one vertex  $v_2$  located on a circuit (i.e., `nodes2.succ  $\neq$  0`) such that the Manhattan distance between  $v_1$  and  $v_2$  is less than or equal to `MAXDIST`.

In order to specify the fact that each vertex is involved in at least one arc we use the graph property `NVERTEX = |NODES|`. Finally the dynamic constraint `nvalues_except_0(variables, =, 1)` expresses the fact that, for each vertex  $v$ , there is exactly one predecessor of  $v$  that belongs to a circuit.

Parts (A) and (B) of Figure 5.233 respectively show the initial and final graph associated with the second graph constraint of the **Example** slot.

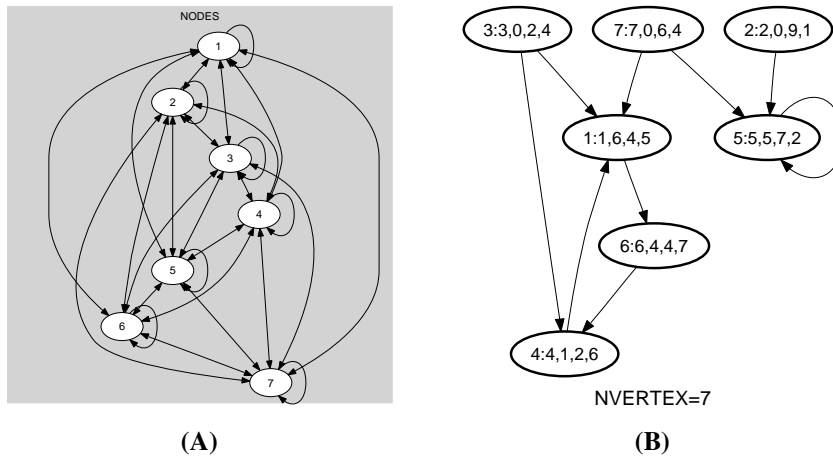


Figure 5.233: Initial and final graph of the cycle\_or\_accessibility constraint

#### Signature

Since  $|\text{NODES}|$  is the maximum number of vertices of the final graph associated with the second graph constraint we can rewrite  $\text{NVERTEX} = |\text{NODES}|$  to  $\text{NVERTEX} \geq |\text{NODES}|$ . This leads to simplify  $\text{NVERTEX}$  to  $\text{NVERTEX}$ .